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Substitution Form 1449/PTO



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application No.	10/811,191
Filing Date	March 26, 2004
First Named Inventor	RUECKES, et al.
Art Unit	2818-2823
Examiner Name	TBA Michelle Estrada
Attorney Docket Number	112020.147 US2 NAN-23

Sheet 1 of 1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
[Signature]		US-2005/0041465 A1	02-24-2005	RUECKES, et al.	
		US-2005/0041466 A1	02-24-2005	RUECKES, et al.	
		US-2005/0056877 A1	03-17-2005	RUECKES, et al.	
		US-6,548,841	04-15-2003	FRAZIER et al.	
		US-6,803,840	10-12-2004	HUNT et al.	
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		US-			
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[Signature]		WO 03/091486 A1	11-06-2003	Nantero, Inc.	
		WO 04/065655 A1	08-05-2004	Nantero, Inc.	
		WO 04/065657 A1	08-05-2004	Nantero, Inc.	
		WO 04/065671 A1	08-05-2004	Nantero, Inc.	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

Examiner Signature	Michelle Estrada	Date Considered	12/12/05
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		Number-Kind Code ^{2(If known)}			
Age		US-3,448,302	06-03-1969	SHANEFIELD	
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			Art Unit	2818 2823	
			Examiner Name	TBA M. Estrada	
Sheet	2	of	4	Attorney Docket Number	112020.147US2 NAN-23

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US-2004/0214366 A1	10-28-2004	SEGAL et al.
US-2004/0214367 A1	10-28-2004	SEGAL et al.

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		Number-Kind Code ^{2(f known)}			
ASE		WO 01/44796 A1	06-21-2001	Board of Trustees of Leland Stanford Junior University	
ASE		WO 01/03208 A1	01-11-2001	President and Fellows of Harvard College	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
ASE	A1	CHOI, W.B. et al., "Carbon-nanotube-based nonvolatile memory with oxide-nitride-film and nanoscale channel," <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(2), pp. 275-277.		
	A2	DEQUESNES, M. et al., "Calculation of pull-in voltages for carbon-nanotube-based nanoelectromechanical switches," <i>Nanotechnology</i> , 2002, Vol. 13, pp. 120-131.		
	A3	DEQUESNES, M. et al., "Simulation of carbon nanotube-based nanoelectromechanical switches," <i>Computational Nanoscience and Nanotechnology</i> , 2002, pp. 383-386.		
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	A5	WOLF, S., Multilevel-Interconnect Technology for VLSI and ULSI, 1990, Lattice Press, Sunset Beach, pp. 189-191.		
ASE	A6	TOUR, J. M. et al., "NanoCell Electronic Memories," <i>J. Am. Chem Soc.</i> , 2003, Vol. 125, ppl 13279-13283.		

Examiner Signature	<i>M. Estrada</i>	Date Considered	12/12/05
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			Art Unit	2818 2823
			Examiner Name	TBA M. Estrada
			Attorney Docket Number	112020.147US2 NAN-23
Sheet	3	of	4	

hpl	A7	RUECKES, T., et al., "Carbon Nanotube-Based Nonvolatile Random Access Memory for Molecular Computing" <i>Science</i> , 2000, Vol. 289, pp. 94-97.	
	A8	FAN, S. et al., "Carbon nanotube arrays on silicon substrates and their possible application," <i>Physica E</i> , 2000, Vol. 8, pp. 179-183.	
	A9	ZHAN, W. et al., "Microelectrochemical Logic Circuits," <i>J. Am. Chem. Soc.</i> , 2003, Vol. 125, pp. 9934-9935.	
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	A11	KINARET, J.M. et al., "A carbon-nanotube-based nanorelay", <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(8), pp. 1287-1289.	
	A12	FRANKLIN, N. R. et al., "Integration of suspended carbon nanotube arrays into electronic devices and electromechanical systems," <i>Appl. Phys. Lett.</i> , 2002, Vol. 81(5), pp. 913-915.	
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	A14	DAI, H. et al., "Controlled Chemical Routes to nanotube Architectures, Physics, and Devices," <i>J. Phys. Chem. B</i> , 1999, Vol. 103, pp. 111246-11255.	
	A15	HOMMA, Y. et al., "Growth of Suspended Carbon Nanotubes Networks on 100-nm-scale Silicon Pillars," <i>Appl. Phys. Lett.</i> , 2002, Vol. 81(12), pp. 2261-2263.	
	A16	AJAYAN, P.M., et al., "Nanometre-size tubes of carbon," <i>Rep. Prog. Phys.</i> , 1997, Vol. 60, pp. 1025-1062.	
	A17	SREEKUMAR, T.V., et al., "Single-wall Carbon Nanotube Films", <i>Chem. Mater.</i> 2003, Vol. 15, pp. 175-178.	
	A18	VERISSIMO-ALVES, M. et al., "Electromechanical effects in carbon nanotubes: <i>Ab initio</i> and analytical tight-binding calculations," <i>Phys. Rev. B</i> , 2003, Vol. 67, pp. 161401-1 - 161401-4.	
	A19	FUHRER, M.S. et al., "High-Mobility Nanotube Transistor Memory," <i>Nano Letters</i> , 2002, Vol. 2(7), pp. 755-759.	
	A20	RADOSAVLJEVIC, M. et al., "Nonvolatile molecular memory elements based on ambipolar nanotube field effect transistors," <i>Nano Letters</i> , 2002, Vol. 2(7), pp. 761-764.	
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	A23	LEE, K.H. et al., "Control of growth orientation for carbon nanotubes," <i>Appl. Phys. Lett.</i> , 2003, Vol. 82(3), pp. 448-450.	
	A24	CASAVANT, M.J. et al., "Neat macroscopic membranes of aligned carbon nanotubes," <i>Journal of Appl. Phys.</i> , 2003, Vol. 93(4), pp. 2153-2156.	
	A25	AMI, S. et al., "Logic gates and memory cells based on single C ₆₀ electromechanical transistors," <i>Nanotechnology</i> , 2001, Vol. 12, pp. 44-52.	
	A26	DEHON, A., "Array-Based Architecture for FET-Based, Nanoscale Electronics," <i>IEEE Transactions on Nanotechnology</i> , 2003, Vol. 2(1), pp. 23-32.	

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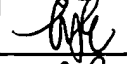

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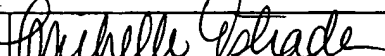
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	A27	TANS, S. et al., "Room-temperature based on a single carbon nanotube," <i>Nature</i> , 1998. Vol. 393, pp. 49-52.	
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